



SAI INSTITUTE OF TECHNOLOGICAL SCIENCE, CHOUUDWAR, CUTTACK-754027
Approved by AICTE and Affiliate to SCTE&VT, BBSR, ODISHA



CLASS SCHEDULE FOR 4TH SEMESTER				BRANCH- ELECTRICAL		Session:2025(SUMMER)		W.E.F-04.02.2025	
DAY	1 st 9:00-9:55 AM	2 nd 9:55-10:50 AM	3 rd 10:50-11:45 AM	4 th 11:45-12:40 PM	12:40-01:40 PM	5 th 1:40-2:35 PM	6 th 2:35-3:30 PM	7 th 3:30-4:25 PM	
MONDAY	E C-I (M.R.Pati)	EM& I (D.Baidya)	AE& OP-Amp (D.Biswal)	GT&D (S.Das)	L U N C H	Electrical Machine Lab-I(GR-I) (J.D.Panda & S.Behera)			
						Electrical Drawing (GR-II) (S.Das)			
TUESDAY	Analog Electronics Lab(GR-II) (D.Biswal & S.Behera)			E C-I (M.R.Pati)	B R E A K	EM& I (D.Baidya)	SCA (P.SWAIN)	GT&D (S.Das)	
	Simulation Practice on MATLAB(GR-I) (D.Baidya)					Simulation Practice on MATLAB(GR-II) (D.Baidya)			
WEDNESDAY		EM& I (D.Baidya)		GT&D (S.Das)		Analog Electronics Lab(GR-I) (D.Biswal & S.Behera)			
THURSDAY	E C-I (M.R.Pati)	EM& I (D.Baidya)	AE& OP-Amp (D.Biswal)	GT&D (S.Das)		Electrical Machine Lab-I(GR-I) (J.D.Panda & S.Behera)			
						Electrical Drawing(GR-II) (S.Das)			
FRIDAY	E C-I(T) (M.R.Pati)	AE& OP-Amp (D.Biswal)	SCA (LIBRARY STUDY) (P.Swain)	EM& I(T) (D.Baidya)	B R E A K	Electrical Machine Lab-I(GR-II) (J.D.Panda & S.Behera)			
						Electrical Drawing(GR-I) (S.Das)			
SATURDAY	Electrical Machine Lab-I(GR-II) (J.D.Panda & S.Behera)			E C-I (M.R.Pati)		SCA (P.Swain)	AE& OP-Amp (D.Biswal)		
	Electrical Drawing(GR-I) (S.Das)								

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SUBJECTWISE FACULTY LIST:- Th.1 Energy Conversion-I- M.R.Pati,Th.2 Analog Electronics & OP-Amp- D.Biswal ,Th.3 Electrical Measurement & Instrumentation- D.Baidya, Th.4- Generation, Transmission and Distribution- S.Das.

Pr.1 Electrical Machine Lab-I – J.D.Panda & S.Behera, Pr.2 Analog Electronics Lab- D.Biswal & S.Behera, Pr.3 Simulation Practice on MATLAB- D.Baidya,

Pr.4 Electrical Drawing- S.Das & S.Behera

GR-I: F23135002001-F23135002047(29) GR-II: L24135002001 – L23135002023(23)

Copy to: - Office, Concern person, Staff Common Room, Notice Board

N.B:- The Classes will continue as per the above time table until the further order

HOD (ELECTRICAL ENGG)
 SAI INSTITUTE OF TECHNOLOGICAL SCIENCE